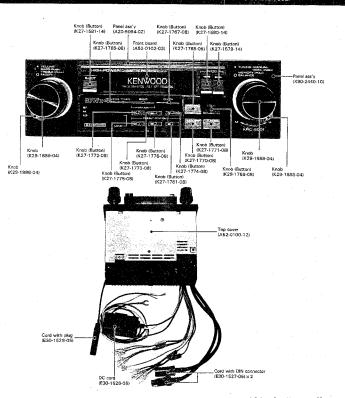
KRC-5001 SERVICE MANUAL

KENWOOD

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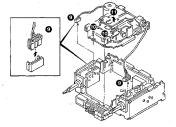




DISASSEMBLY FOR REPAIR

- 1. Remove 2 screws 1 retaining the top cover.
- 2. Remove the top cover in the direction of arrow 2.
- 3. Remove 4 screws 3 retaining the front cover.
- 4. Remove the front cover in the direction of arrow 0 .
- 5. Remove 6 screws 6 retaining the bottom plate.
- 6. Remove the bottom plate in the direction of arrow 6.7. Remove 6 screws 7 retaining the head sink.
- Remove to screws retaining the nead sink.
 Remove the heat sink in the direction of arrow

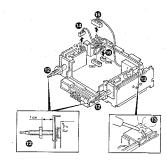
- 1. Disconnect connectors CN1 and CN6 (9).
- 2. Remove 2 screws retaining the mechanism block.
- Remove the mechanism block in the direction of arrow (1).





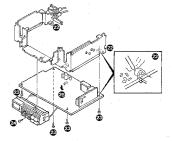
DISASSEMBLY FOR REPAIR

- Loosen 2 nuts 10 retaining the right and left variable resistors by 1 cm.
- 2. Remove the solder as shown in 18.
- 3. Disconnect connectors CN4 (1), CN7 (1) and CN6 (1).



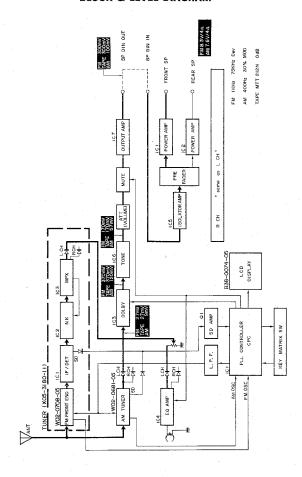
- 1. Remove right variable resistor (1).
- 3. Remove left variable resistor (B)
- 4. Remove screw @ retaining the PC board.
- 5. Remove the PC board in the direction of arrow @

- 1. Remove solder from 2 portions @ .
- 2. Remove 4 screws @ retaining the bottom PC board.
- 3. Remove screw @ retaining the switch block.
- Remove the bottom PC board in the direction of arrow

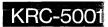




BLOCK & LEVEL DIAGRAM







Description of Components

TUNER UNIT (X05-3180-11)

Component	Use & Function	Operation, Condition & Compatibility
IC1	FM IF Detection	
IC2	Noise Canceller	
IC3	MPX	
QT	LOCAL/DX SW	
02, 3	IF Gain	
Q5	ANRC Buff.	
Q6	CRSC Driver	

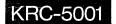
CONTROL UNIT (X11-2340-10)

Component	Use & Function	Operation, Condition & Compatibility			
IC1, 2	Power amp				
IC3	DOLBY B Hitschi HA120476 H-IC.				
IC4	TAPE EQ. MET switch, MUTE circuit built-in.				
IC5	T.ADV	For blank detection.			
IC6	TONE	Tone control H-IC.			
IC7	PRE OUT AMP	D IN OUT.			
Q1	T.ADV PL driver				
02	T.ADV SW				
Q3, 4	LOUDNESS SW	LOUDNESS SW			
Q5, 6	POWER IC standby SW				

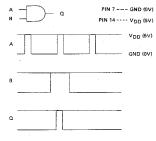
SYNTHESIZER UNIT (X14-2010-10)

Component Use & Function Operation, Condition & Compatibility		Operation, Condition & Compatibility	
IC1	Microprocessor .		
IC2	T.CALL control	T.CALL, T.ADV, DOLBY and MUTE control.	
IC3	Key input control	FWD/REV, ST lamp and SD T/R switch.	
IC4	Key matrix	UP/DOWN, MEMORY and SEEK input switch.	
IC5	Isolation amp	For power amp input.	
Q1	SD signal inverter	ON when SD is present.	
Q2	MET SW	OFF for MET ON.	
O3	TAPE muting	Muting for TAPE EQ IC.	
Q4	Muting driver		
Q5	T.ADV SW	ON for T.ADV.	
Q6	FM +B SW	ON for FM.	
Q7	AM +B SW	ON for AM.	
Q8, 9	Radio switch	AM/FM/TAPE switching.	
Q10, 11	Regulator	Regulated power supply for 9 V line.	
012	Chip Enable	Microprocessor operation QN/OFF.	
013	AVR	Vop 5 V power supply.	
Q14, 15	LPF		
Q16	LPF gain SW	ON for FM.	
Q17, 18, 20	Muting	Signal line muting.	
019	TAPE mode SW	ON for TAPE mode.	
021, 22	Power control	For standby of POWER IC.	





AND-GATE For CPU Key Matrix Operation Description



CPU Key Matrix Operation

The source clock from the CPU is input to A-input via the AND-GATE at any time to apply the control signal to B-input.

When the signal is input to B-input, the output Q goes high and input as the CPU key input. When the B-input is low level, output Q is always low. Output Q is synchronized with input A.

Synthesizer Unit μ -Com: μ PD 1708G

FUNCTION OUTLINE

Receiving frequency, Channel spacing, Reference frequency, Intermediate frequency

FM band

AM band

Frequency range	Channel spacing	Reference frequency	Intermediate frequency
87.50~108.0 MHz	* 50 kHz	. 12.5 kHz	10,700

^{&#}x27; MANUAL 25 kHz

٠	Frequency range	Channel specing	Reference frequency	Intermediate frequency
	522~1611 kHz	9 kHz	9 kHz	450 kHz
	153-281 kHz	* 9 kHz	1 kHz	450 kHz
				

^{*} MANUAL 1 kHz

Tuning Function

- Auto Tuning (Sawtooth wave mode)
 Seek Up: Once a station is tuned, it is held tuned.
- (2) Manual Tuning (Sawtooth wave model

Manual luning (Sawtooth wave n Manual Up/Down: Frequency

Frequency is advanced up or down in steps by pressing the oush switch

push switch.

Pressing for a half second or

more advances it up or down continuously until the switch is

released.

(3) Preset Memory Recall 6 stations on each FM. MW, and LW band can be preset independently with the 6 buttons. The last station is stored in memory for each band when power is turned off.

Tape Function

- (1) Tape running indicator
- (2) METAL control

Radio Function

(1) MONO control

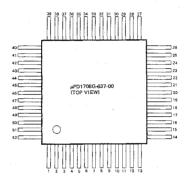
Other Functions

(1) I OUDNESS control



TERMINAL DESCRIPTION

Terminal Configuration (Top View)



Pin No.	Pin Name	Pin No.	Pin Name
1	LCD4	27	KS, (PB,)
2	LCD3	LCD3 28 KS ₀ iP	
3	LCD2	29	BANDZ/N.R
4	LCD1	30	METAL-LOC
5	COM2	31	LOUDNESS
6	COM1	32	
7	V _{DD}	33	•
8	FM	34	
9	AM	35	
10	GND	36	
11	EO,	37	•
12	EO,	38 LCD19	
13	CE 39		LCD18
14	•	40	LCD17
15	XI	I 41 LCD16	
16	XO	42 LCD15	
17	AF MUTE (PA ₃)	43	LCD14
18	BAND 1 (PA.)	44	LCD13
19	KS,/K, (PA,)	45	LCD12
20	KS,/K, (PA,)	46	LCD11
21	K ₈	47	LCD10
22	K ₂	48	LCD9
23	К,	49	LCD8
24	K _a	50	LCD7
25	KS, (PB,)	51	LCD6
26	KS, (PB,)	52	LCD5

* Not used.



Symbol

CIRCUIT DESCRIPTION

Pin description

1-4	LCD1	LCD segment signal	LCD segment signal output pin (1/2 duty, 1/2 bias LCD should be used. Frame frequency: 100 Hz, Drive voltage: VDD)		
34~52	LCD23		Trequency, 100 12, and totalga.		
5 6	COM2 COM1	LCD common signal	LCD common signal output pin		
7 33	V _{DD}	Power input	Device power supply prime properties and provided the provided via those pins. Either of them can be used for supplying the power individually. The rising time of VDD should be less in 800 ms (0 to 4.5 V). When the rising time is to long, or when the VDD is not lowered completely to 0 vand then rising the 1.5 Vot long, or when the VDD is not lowered completely to 0 vand then rising the 1.5 Vot most provided the VDD is not lowered completely to 0 vand then rising the 1.5 Vot mit be voltage lower than the prevailing risk, the diodes existing condition for in relationation is not read out correctly in such cases, use the 0.E prin so that the diode switch status can be read out for installization.		
8	FM	FM VCO input	This pin inputs the FM station output signal. Since it incorporates the AC amp, cut the DC signal with the capacitor.		
9	AM	AM VCD input	This pin inputs the AM station output signal. Since it incorporates the AC amp, cut the DC signal with the capacitor.		
10 -	GND	Ground	Connect to the ground terminal of the set.		
11	EO,	Error Out	Charge pump output of the phase detector consisting of PLL. When the frequency divided by the osalisting frequency is higher than the reference frequency, these pins output high hevel signals, and when it is lower than the reference frequency, there go low. When the frequency (divided by the osalisting frequency) is coincided with the reference frequency, it enters into the floating status.		
13	CE	Chip Enable	This pin is used to input the selected signal from the device. When operating the PL section, this pin goes high, and when the PLL section is stopped, it goes low. When low level, the display goes off. However, a low level signal below 134 µs or high lev signal is not accepted.		
15 16	XI XO	Crystal resonator	Connectors of the crystal resonator. Connect the 4.5 MHz crystal resonator.		
17	AF MUTE	Mute Out	This pin outputs the muting signal to eliminate shock noise when the PLL is unlocked and pop noise when switching between Tage and Radio, and is active flow. (CMOS output For siming details, refer to the AF Mute Out Timing Chart. When the CE pin is low, this pin is active flow.		
18	. BAND ₁ .	Band Out	FM/MW switching output pin FM: Hgh WY: Low When the MODE switch is set to "1" (Tape mode), this pin is low. When the MODE so So is provided, follow the SDK section.		
19	KS ₅ /K ₅	Key return signel source and Key return signel input	This becomes the source of key return signal to read out the diode matrix for		
20	KS ₄ /K ₄	Key return signal source and Key return signal input	This becomes the source of the key return signal to read out the diode matrix for initialization only within the power is turned on for the first time IVDD rising time) or when returning from the back-up condition (CE goes high from low). Then, this inputs the key return signal for the key matrix, linser the pull-down resistor (EMOS input fourpe.)		
21	, K*	Key return signal input	This pin inputs the key return signal for the key matrix. Insert the pull-down resistor.		
24	K _b		(CMOS inpu		
25 28	KS ₁ \ KS ₄	Key return signal source	This pin outputs the key return signal for the key matrix. Since the synchronous current is greatly lowered because of its configuration, the reverse-current prevention diode will be not necessary for the key source side. (CMMOS output		
30	METAL/DX/LOC	LOC Out	In radio mode: DX/Local On/Off output pin When *LOC" is displayed on the LCO panel, high level signal is output. When it is not bouleved signal is output. When the power is turned on, low level status is initialized In tape mode: METAL On/Off output pin When *METAL is displayed on the LCO panel, low level signal is output. When it is n It, high level signal soutput. On initialization when the tape power is turned on, high level is output.		



Pin No.	. Symbol Pin Name		Description		
31	LOUDNESS	Loudness Out	LOUDNESS output, in When "1.0UD" is displayed on the LCD panel, low level signal is output. When it is not it, high level signal is output. When the power is turned on first (VDD rising time), low level signal is output. (CMOS output)		
32			DOLBY output pin. When "DOLBY" is displayed on the LCD panel, high level signal is output. When it is no iit, low level signal is output. On initialization when the power is turned on, low level is output.		

RAND2/NR

When Band A is "0" or "1" and the NR selector is "1", this functions as the NR or/off output pin, When "NR" is displayed on the LCD panel, high level signal is output. When it is not lift, low level signal is output.

This pin can be operated in the TAPE/RADIO mode.

On initialization when the power is turned on, this pin is at

When BAND A is "0", "1" and the NR selector is "0", this function as the WIDE-ADV on/off output pin.

. In the Radio mode:

This functions as the WIDE on/off output pin. When "WIDE" is displayed on the LCD panel, high level signal is output, and when it is not lit, low level is output.

In the Tape mode:

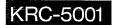
This functions as the ADV on/off output pin.

When "ADV" is displayed on the LCD panel, high level signal is output, while it is not lit, low level is output.

On initialization when the power is turned on first, it is at low level.

When BAND A is "0" and the NR selector is "0" (SDK operation is normal only when in this status), and BAND 8 is "1". This pin functions as the BAND 2 output BAND 2 becomes the band switching output port in combination with BAND 1.

Output Mode	BAND 1	BAND 2
MW	Ĺ	L
FM	Н	L
LW	L	н
SDK	Н	Н



1. KEY MATRIX CONFIGURATION

1-1. Key Matrix Layout

Input pin Output pin	K _s (19)	K, (20)	K ₃ (21)	K ₂ (22)	K, (23)	K _q (24)
KS _q (28)	SEEK DOWN	SÉEK UP		LOUDNESS	MTL	моно
KS, (27)	MO	MU	M4	M3	M2	. M1
KS ₂ (26)	ME	SDK	M6	M5		SAND
KS _s (25)		SK	MODE	SD	ST	FOW/REV
KS, (20)			CLK/FRQ:	NR SEL	BAND B	
KS ₆ (19)			BAND A	PRIORITY	BAND C	CLKSEL

The number in the bracket shows the pin no.



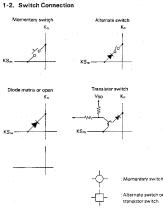
: Momentary switch

Alternate switch or transistor switch

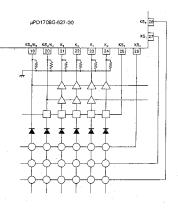


: Diode matrix (closed/opened by diode)

: Open



1-3. Key Matrix Connection





2. KEY MATRIX DESCRIPTION

2-1. Diode Matrix for Initialization

The diode matrix for initialization has the following five status. All status is read out only when the power is supplied to the Vop for the first time (Power-ON, Reset) and when the CE pin' goes high from low level (CE Reset), in another periods, the diode matrix status is ignored.

 The switch for setting the receiving frequency range and the channel spacing:

BANDA

(2) Clock signal select switch:

CLKSEL

(3) Priority select switch for display:

PRIORITY

(4) ___

(5) NR select switch:

NR SEL

(6) CLOCK/FREQUENCY select switch:

CLOCK/FRQ

(7) · LW select switch:

BAND 8

This switch is status is as for BAND A 1 1 0	Frequency Range 87.9~107.9 MHz	Channel Spacing	for each FM/MW/LW Manual Step	band channel spacing. Each setting
1	87.9~107.9 MHz		Manual Step	
1				
<u> </u>		200 kHz		
0 -	530~-1620 kHz	10 kHz	-	
	87.5~108.0 MHz	50 kHz	25 kHz	
0	522~1611 kHz	9 kHz		
When the display which does not have priority is recalled, the mode having priority will be displayed after appro 5 seconds. "1". No priority "0". Priority is provided. Select switch to provide the clock function or not. "1": Clock fen or provided fire back-up. (AAM in not cleared.				
"O": Clock is provided. Select switch to provide the NR (noise reduction) function. "O": NR is not provided (WIDE-ADV and BAND 2 output pin) "": NR provided				
Select switch to provide priority to the clock or frequency for display (Depending on PRIORITY) "0": Frequency "1": Clock				
"0": M1 to Mi	6 keys are preset indeper	idently preset memory is sent seq		
	When the dif- 5 seconds. "11": No priori "0": Priority is Select switch "10": Clock is "00": Clock is "00": NR is no "11": NR provi Select switch "01": Frequence "11": Clock Switch to acc. "00": M1 to M	When the display which does not it is seconds. "In" No priority """: Priority is provided. Select switch to provide the clock face """: Clock is not provided the Clock in """: Clock is provided. """: Clock is not provided the RIT (noise """)": Rit provided. """: "In """: Rit provided "WIDE-ADV ar """: "Rit provided "WIDE-Face Var """: """: """: """: """: """: """: ""	When the display which does not have priority is receiled. 5 seconds. "In No priority "Or, Protrick is provided. Select switch to provide the clock function or not. "In". Clock is no provided the clock function or not. "Or. Clock is provided. "Or. Clock is provided the NIT close in education function. "Or. NIS is not provided the NIT close in education function. "Or. NIS is not provided the NIT close in education function. "Or. NIS is not provided the NIT close in education function. "Or. Provided. Select switch to provide priority to the clock of frequency for dis. "Or. Properties. "Or. Explaints of the clock of frequency for dis. "Or. Properties. "Or. Clock Switch to access the preset mismory NIT to Mills sequentially. "Or. MIT to Milk keys are preset mismorphism."	5 seconds. '1": No priority provided. Select swint his provide the clock function or not. "1": Cock is not provided if by back-up, RAM is not cleared: "0": Cock is positioned. Select swinth to provide the MR robes naturation function. "0": Not is not provided the MR Robes naturation function. "0": Not is not provided wIDE-ADV and BAND 2 output pink "1": NR provided. Select swinch to provide priority to the clock or frequency for display (Depending on PRI "0": Frequency "1": Clock Swinch to access the preset memory (M1 to M6) sequentially



2-2. Mode Select Switches

Unlike the initializing switches, these switches can be changed at any times (On the following table, "1" shows switched ON, "0" shows switched OFF.)

Symbol	Function Description
MODE	Set the unit to RADIO mode or TAPE mode. "1": TAPE mode "0": RADIO mode
SD	In the RADIO mode: This is the Station Detector input in SEEX or SCAN mode This should be set to DFF within approx. 50 ms after the PLL is locked. When every times are OFF by detecting the station every 1 ms, the station is recognized as received and the seeking or scanning operation stops.
STEREO	In the RADIO mode: (Only for FM reception) Stereo signal input switch. When this switch turns OFF, "ST" is displayed on the LCO panel. However, "ST" goes off in the Auto Tuning mode (AF-MUTE prin's active) even if this switch is OFF.
FOW/REV	In the Tape mode: Tape running direction indicator input switch. When this switch turns ON, the "REV" (◀) is displayed on the LCD panel. When its turns OFF, the "FOW" (▶) is displayed. This switch functions only when the CF pinis high and the MODE switch is "ON" (Tape mode).

2-3. Momentary Switches

Symbol	Function Description								
MU MD	Frequence Each time (channel until it is r Clock (time)	e the key is pressed, the o spacing set!. When it is pre eleased.	displayed for a	equency in half secon	d or more,	the freque	ncy is adva	nced rapidl	y (continuo)
M1 \$ M6	FM, MW and (1) When v With th quency (2) When n When d	re used to write or recall the LW bands can be stored in criting a frequency display, within currently received into men	five second nory. ed, the mem the VOD is	into each s after pre- cony conter irst turned	ssing the A	IE key, pres ryl correspo	anding to the	e key press	ed is raculle.
	Band	Preset Memory Key Vency Range	M1	M2	мз	M4	M5	М6	
		87.9~107.9 MHz	87.9	90:1	98.1	106.1	107.9	87.9	
	FM	87.50~108.00 MHz	87.50	90.1	98.1	106.1	108.00	87,50	
	i 1								
	MW	530~1620 kHz	530	600	1000	1400	1620	530	



Symbol	Function Description
M1 ; M6	These keys are used to write and recall the present memory. Each RM, MVM and VM respenser, come has sorted into not key in memory independently. However, the number of available bands differ with the area designated by the initializing diode matrix, as follows: For the area Disense are availables: 6 stations $\times 2 = 12$ stations For the area Disense are availables: 6 stations $\times 3 = 18$ stations Corresponding to the present key present, the "CP" indicator and ""channel numbers are displayed on the LCD panel.
SEEK UP/DOWN	These keys are used for automatic tuning. During auto tuning operation, when the SD switch is turned OFF, the frequency displayed at the time is kept on hold. In auto tuning mode, the auto axing operation is continued even when the LOUONESS, ME, NR, METAL-DX/LOC, or MONO-OOLBY key is presend. When one of the other keys is presend, the auto turning operation is stopped, and the upit enters the operation of the key presend. When the SEEK key is presend again, the frequency before the SEEK operation is resumed.
DX/LGC MTL	This key is used to select the function between DX/LOC — MTL In the Radio mode: Each frem the Key is pressed, the LOC output pin and the "LOC" display on the LCD panel and inverted. When the "LOC" display on the LCD panel, high level signal is output from the LOC out pin, and when it is not it, low level is output. In the Tape mode: Each frem the key is pressed, the LOC output pin and the "MTL" display are inverted. When the "MTL" is displayed on the LCD panel, low level signal is output from the LCD output in the LCD panel, low level signal is output. By intelligation when the power is surred on, high level signal is output.
ME	This key is used for writing the preset memory. It is also used for edijusing the time on clock display. Used when writing a new frequency into the preset memory. When this key is pressed, the "ME" is displayed on the LCD patel, and is for five seconds after the key is reliased. While the "ME" is II, pressing one key (M1 to M6) stores the displayed frequency into memory corresponding to the key pressed. To cannot the preset memory, while the "ME" is II, pressing yet yet the than ME, NE, METAL-DX/LOC, MONO-DOLBY, or LOUNRES. Clock display: The "hour" and "minutes" can be adjusted by pressing the MD or MU key while pressing the ME key. After pressing the ME key, each time the M0 key is pressed, the "hour" is advanced one by one, Pressing it for a half second or more advances the time by 4 hours/sec continuously until the M0 key is released. This operation does not affect the "minute" or "second" digits they are not disciplayed during this operation. After pressing the ME key, acch time the MU key is pressed, the "minute" is advanced one by one. Press it for a half second or more advances the mustile in Birutes/sec speeds continuously will the M0 key is released. The "second" is not displayed, however, it is reset to zero every time the "minute" is at will the M0 key is released. The "second" is not displayed, however, it is reset to zero every time the "minute" is ex. The "minute" adjusting does not affect the "nout". ("Hour" is not changed even when the "minute" cause do 1). (During dock display, pressing the ME key alone changes the display to hequency and "ME" is displayed in the key pressed.)
BAND	This key is used to select the band. When Sand A is "0" or "1" and Band B is "0" (LW: Not available) Each time this key is pressed, the band is changed in the order of FM — MW — FM
LOUDNESS	Used for Loudness select Ney. Each time this keys pressed, the brudness output pin and the "LOUD" display on the LCD panel are inverted. When the "LOUD" is displayed on the LCD panel, low level signal is output from the Loudness pin and when it is not lit, high level is output. We initialization when the power is first turned on initiaging time of VDD," (LOUD" is displayed and low level is inotiput.)



Symbol	Function Description
RCAL	Display select key, Available only when in the radio mode. When this key is pressed, the display is changed from the clock display to frequency or vice versa, However, five seconds after the key is pressed. the display is restored to the priority mode (depending on the dioch entatix PRIORITY). When the clock is not provided (CLKSEL=0), this key has no effect. However, the clock display is resumed by the PRIORITY switch when the display priority is provided. a) OR Priority is provided.
NR	(1) Nil key (RADIOTARE Commonkey) (2) WDIC-ADV key independent RADIOTARE key) (1) Nil key: BAND A. "O"."" Nil SEL. """ With the above status, this key is used as the NR select key. Each time the key is present, the BAND2/NR output pin and "NR" display on the LCD panel are inverted. When "NR" is displayed on the LCD panel, the BAND2/NR pin outputs the high tevel, and when the display is not lit, low level is output. (By initialization when the power is turned on, it outputs low sevel.) 12) WDIC-ADV key: BAND A. "O"."" NR SEL. "" With the above status, this key is used as the WIDE-ADV select key. In the Radio mode: Used as the WDIC select key. Each time the key is present, the BAND2/ADV output pin and the "WIDE" display on the LCD panel are inverted. When the "WIDE" is displayed on the LCD panel, the BAND2/ADV pin outputs the high level, and when the display is not lit, low level is output. On Tape mode: Used as the ADV select key. Each time the key is greased, the BAND2/ADV output pin and the "ADV" display on the LCD panel are inverted. When the "ADV" is displayed on the LCD panel, the BAND2/NR output pin and the "ADV" display on the LCD panel are inverted. When the "ADV" is displayed on the LCD panel, the BAND2/NR output pin and the "ADV" display on the LCD panel are inverted. When the "ADV" is displayed on the LCD panel, the BAND2/NR output pin and the "ADV" display on the LCD panel are inverted. When the "ADV" is displayed on the LCD panel, the BAND2/NR output pin and the "ADV" display on the LCD panel are inverted. When the "ADV" is displayed on the LCD panel, the BAND2/NR output pin and the "ADV" display on the LCD panel are inverted. When the "ADV" is displayed on the LCD panel, the BAND2/NR output pin and the "ADV" display on the LCD panel are inverted. When the "ADV" display on the LCD panel are inverted. When the "ADV" displayed on the LCD panel are inverted. When the "ADV" displayed on the LCD panel are inverted. When the "ADV" displayed on the LCD panel are inverted. When the "ADV" displa
M5 BAND	This key is used for setting the received frequency range for FAMAWULVI band and the channel specing. 1. By initialization when the prover is turned on, the receiving frequency and channel specing are registrered by the diode of BAND A. 2. When the CE pin is neverted to high from low while pressing the MS key and the BAND key together, the band setting of BAND A is changed from "1" to "0" or from "0" to "1". Then, when the CE pin is inverted togit—"1"—"15" or vice very and, the changed area setting is maintained. When the CE pin is inverted togit by the Wise by the MS key and BAND key together, the band setting follows the diode of BAND A. too. Then, when the CE pin is inverted from "2"—""1","Cor vice versa, it follows the diode of BAND A. too. Then, when the CE pin is inverted from "2"—""1","Cor vice versa, it follows the diode of BAND A. too. 1. To change the setting by the MS key and the BAND key, repeat procedure 2 and 3. Note: On initialization when the power is turned on, the MS key and the BAND key are ignored even when they are pressed, and the setting is flowed to the diodewed to the



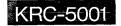
ADJUSTMENT

Set the controls and switches as follows:

BALANCE: center position LOUD : OFF
FADER: center position T-AOV : OFF
BASS : center position METAL : OFF
TREBLE: center position DOLBY NR: OFF

LOCAL AUTO : OFF

30	ITEM	SETTINGS	OUTPOT SETTINGS	RECEIVER SETTINGS	ALIGNHENT POINTS	ALIGN FOR	FIG
F M	SECTION	30111390	35111800	00111100	IVIRIA	ALIUN TUR	1 / 10
1	DISCRIMINATOR	(A) 98.1MHz 0 dev 60dB w Y (ANT imput)	Connect the DC voltageer between pins of TP1.	PM 98.1ME2	T1 (X05-)	d V	
2	PILOT CANCELLER	(A) 98.1MHz 0 dev Pilot:x7.5kHz dev 60dB \(\text{Y(ANT imput)} \)	(B)	PM 98.1MHz	YR7 (XDS-)	Winiam output	
3	SEPARATION	(C) 98.IMEx 1kHz±87.EkHz dev P:lot:±7.5kHz dev Selector:L or R 60dBy Y(kHT input)	(B)	PN 98.1M8z	VR2 (X05-)	Adjust it so that the crosstalk from L to R and R to L become minimum.	
1	ANRC	(C) 98.1MH= 1kH=267.5kHz dey Filot: #7.5kHz dey Selector: Lor R 1.80dR= V(ANT input) 2.55dB= V(ANT input)	(B)	E9 x861,80	†R3 (X05-) Y82 (X05-)	a. Optimum separation. b. The value approx. -ids below the above level.	
		3.30dB± Y(ANT laput) 4. 1~3			YH! (X05-)	c. iOdB wy separation. d. Repeat a to c until the most optimum separation is obtained.	
5	STOP LEVEL	(A) 98.1MHz 0 dev 20dB # V(ANT (aput)	-	FM 98.1982		STOP	
,	SOFT WUTE:	(A) \$8.1MHz 1kHz±75kHz dev 50dBµ¥(ANT imput)	(B)	FM 98. BHz		Set the volume to 0 dBs.	
7 3 M	SOFT MUTE (2) SECTION	ANT OPEN (No Signal)	(8)	F# 98.1MEz		25dBs	
i	STOP LEYEL	(B) 990kHz 400Hz,30% aod 35dB & V(AKT input)	-	A W 990k#2	¥R1 (X14-)	STOP	
c A	SSETTE DE	MET-114((dkHz)	(B)	TAPE PLAY	Head Azimuth screw	Adjust the azimuth for each L-CH/R-CH or FOW/REY Secomes maximum.	



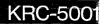
REGLAGES

Régler les controles et les boutons comme suit.

BALANCE : position centre LOUD : OFF
FADER : position centre T-ADV : OFF
BASS : position centre METAL : OFF
TREBLE : position centre DOLBY NR : OFF

LOCAL AUTO : OFF

N.	TEN	REGLAGE DE L'ENTREE	REGLAGE DE LA SORTIE	REGLAGE DU RECELVER	POINTS DE L'ALIGNEMENT	ALIGNER POUR	FIG.
	CTION ME	L ENIXEE	TW SORITE 1	RECEIVER	LAGIUSCHENI	ALIGNER POUR	F16.
ı	DISCRININATEUR	(A) 95.1MRz 0 dév 60dBg: V(Eatrée ANT)	Commecter le voltmêtre CC entre les broches de TP1.	- FM 98.1MHz	T1 (X05-)	94	
2	ANNULATEUR PILOTE	(A) 98,1W52 0 dév Pilote:7.5kHz dév \$0dB#Y(Fairec ANT)	(B)	FY: 98.14Hz	VR7 (X05-)	Sortle minimum	
3	SEPARATION	(C) 98,1MHz 1kM2±67,5kMz dév Pilote:±2,5kMz dév Sélecteur:L ou E 50dB <u>u V</u> (Entrée <u>4</u> NY)	(B)	FM 98.1MHz	YR2 (305-)	L'ajuster pour que la diaphonie de L à R et de E à L devienne minimum.	
		(C) 98.1XHz 1XHz=67.3XHz dev Pilote:±7.3XHz dev Selectour:L ou R			- #B2	a. Séparation optique.	
1	ANRC	(Entrée AST) 2. \$548 \(\mathcal{B} \) (Entrée AST)	(B)	98. 148z	(X05-) YR8 (X05-)	Valeur approximative I db en-dessous du niveau cl-dessus.	
		3. 30dBμΥ (Entrép ANT)			(X82-)	c. Séparation 10 dB.	
		4. i~š				d. Répéter a à c jusqu'à ce que la séparation optimum soit obtenue.	
5	XIVEAU D'ARRET	(A) 98.1MHz 0 dév 20dBµ Y(Entrée AKT)		FM 98,1MHz		ARRET	
6	(1) SITEMCIENX DONY	(A) 88,1Mfz, 1kHzx15kHz dév 60d&4 (Entrés ANT)	(B) ·	FN 98,1#82		Regier le volume sur 0 dB.	
7	SILENCIEUX DOUX (2)	ANT OUVERT (pas de signai)	(B)	FM 98.1MHz		-25dBs	
SE	CTION MA SIVEAU D'ARRET	(8) \$90kHz 480Hz. 30% mod 35dBµ V(Extrée 18T)	-	AM · 990kHz	YRI (XII),	ARRET	
SE	CTION PLA	TINE A CAS					
1	azimut	MTT-114(10xHz)	(B) ⁻	LECTURE DE BANDE	Vis d'azimut de tête	Ajuster Paziadt pour que chaque canal L/canal R ou FOW/REV devienho maximum.	



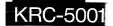
ABGLEICH

Die Regter und Knöpfe wire folgt einstellen.
BALANCE: Mittelage LOUD : OFF
FADER: Mittelage T-ADV : OFF
BASS : Mittelage METAL : OFF
TREBLE: Mittelage DOLBYNR: OFF

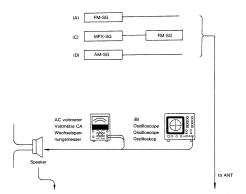
LOCAL AUTO : OFF

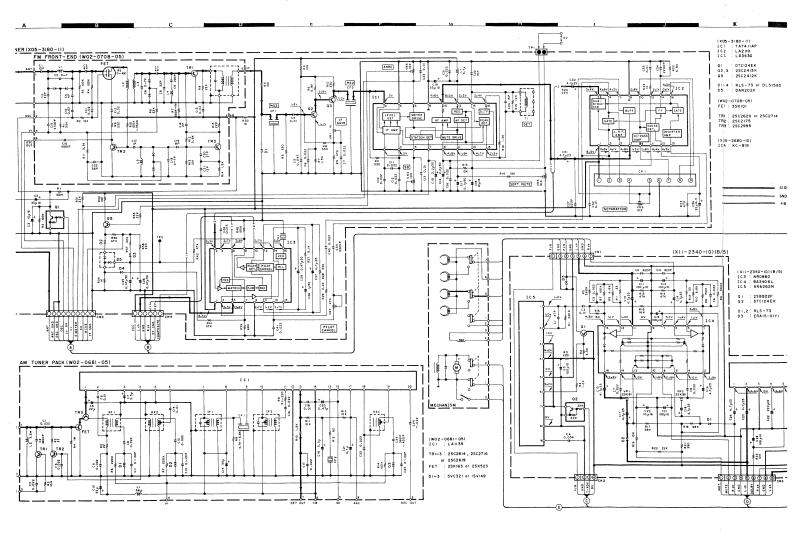
		EINGANGS-	AUSGANGS-	RECEIVER-	ABGLEICH	T	_
MR.	GEGENSTAND	EINSTELLUNG	EINSTELLUNG	EINSTELLDNG	PUNKTE	ABGLETCHEN FUR	ABB.
UK	W~ABTEILU	N G					_
1	DISKRIMINATOR	(A) 98.1MH2 0 Hub 60dB/4 V(ANT-Eingang)	Dem Gleichstrom- Voltmesser zwischen den TPI-Stiften anschließen.	FM 98.1982	71 (X05-)	OV	
2	PILOTZEICHEN - LÖSCHER	(A) 98.1MEz 0 Hub Pilot:±7.5kHz Hub 50dBµV(AST-Eingaag)	(B)	FM 98,1MHz	YR7 (X05-)	Minimeter Ausgang	
3	TREVNUNG	(C) 98.1MH2 1%H2±67.5%H2 Hcb Pifot:±87.5%H2 Mub YMblor:U oder R 800B w V(AST-Elagang)	(B)	FM 98.1982	¥R2 (X05-)	So cinstellen, daß das Bbersprechen von Lasch R und von R nach minimal ist.	
4	AMEC	(C) 95. 1985 1587:875 5566 100 1587:875 5566 100 YEBHET! SEE 116 1688:17 (AST-SIESBEE) 2. 5568:17 (AST-SIESBEE) 2. 5568:17 (AST-SIESBEE) 1. 1~3	(B)	F型 98.1%能2	YR3 (A05-) YR3 (A05-) YR1 (A05-)	a. Optimale Trennums. b. Der Wert liest etwa -1 dB unter dem obliem Wert. c. Trennums (0 dB. d. Die Schritte a bin c wiederboine, bis die optimale Kanairenaums erreicht in	
5	STÖPPEGEL	(A) 98.1MHz 0 Hub 20dB # Y(ANT-Elagang)	-	FM .98,1%Hz		STOP	
6	MEICHE DÄMPFUNG (I)	(A) 98.IMBz IkHz±75kHz Hub 60dBy V(ART-Eingang)	(8)	FM 98.1862		Die tautstärke auf 0 dB einstellen.	
7 M W	MEICHE DAMPFUNG (2) ~ A B T E I L U N	ANT OPEN (Kein Signal)	(8)	FM 98,1%H2		-25dBs	
1	STOPPEGEL,	(D) 9909Hz 400Hz, 30% mod 35dBy V(ART-Eingang) ECK - ABTEIL	UNG -	NF 990kHz	YR1 (X14)	STOP	
1	AZIMUT	MTT-114(10kHz)	(8)	CASSETTEN -	Toskopf Azimut Schraube	Mazimut suf Maximierung yon L-CH/R-CH oder FOR/REV einstellen.	

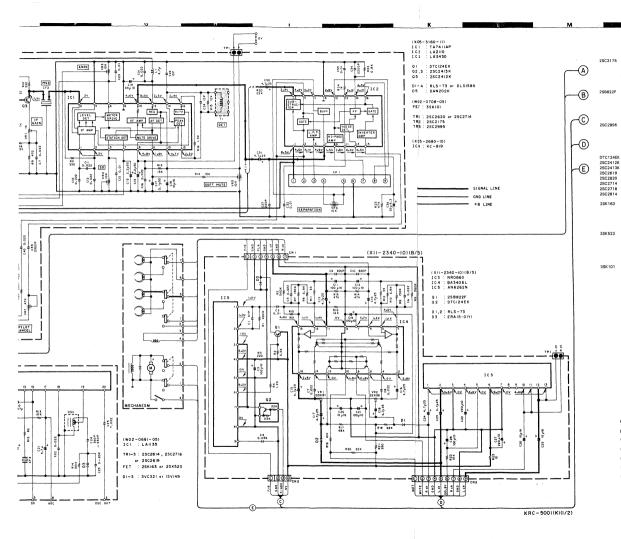




ADJUSTMENT/REGLAGES/ABGLEICH







DC voltages are as measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.

AN6262N

LA1135

LA2110 LA3430

BA3406L

KC-819

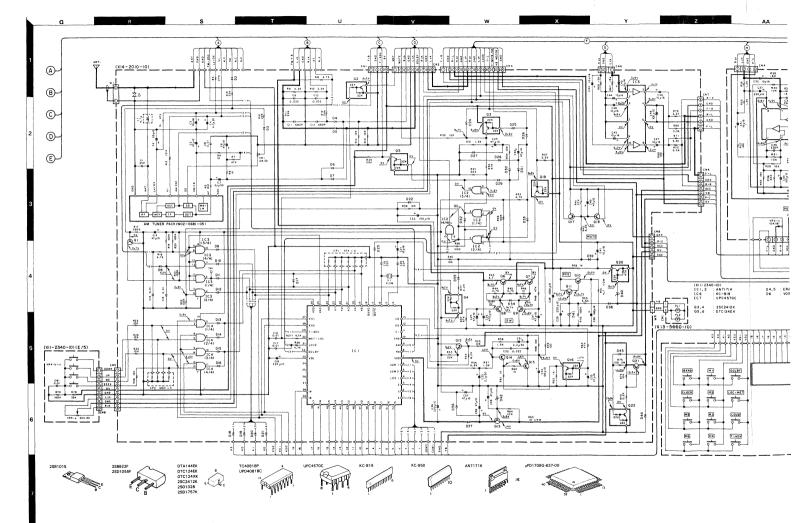
NROSEC

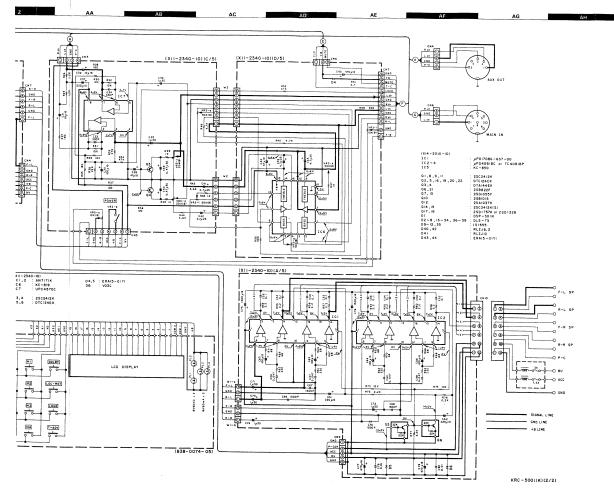
Les tensions c.c., doivent être mesurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.

Die angegebenen Gleichspannungswerte wurden mit einem hochohmigen Spannungsmesser gemessen, Dabei schwanken die Meßwerte aufgrund von Unterschieden zwischen einzelnen Instrumenten oder Geräten u.U. geringfüglig

CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). A indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.







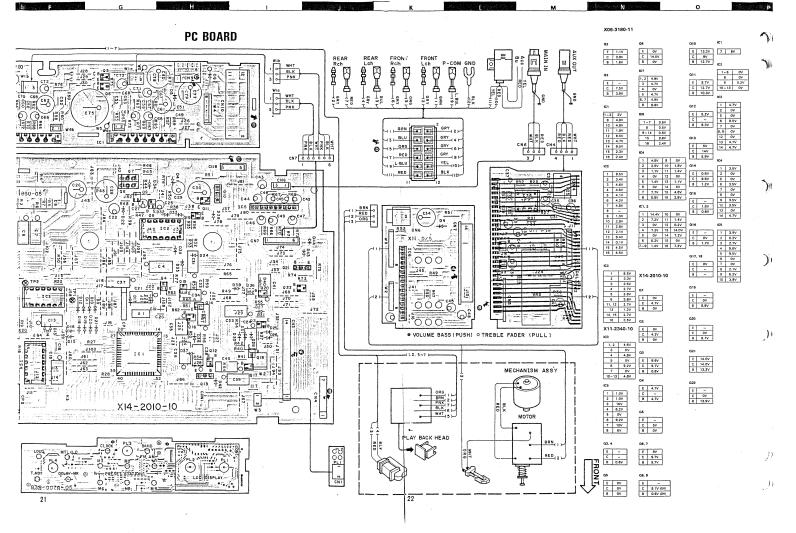
DC voltages are as measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.

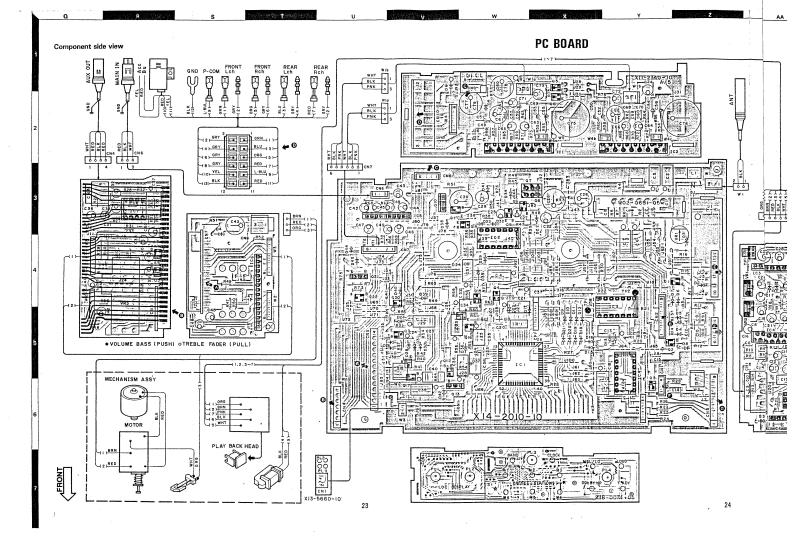
Les tensions c.c., doivent être mesurées avec un voltmêtre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.

Die angegebenen Gleichspannungswerte wurden mit einem hocholtmigen Spannungsmesser gemessen. Dabei schwanken die Meßwerte aufgrund von Ureschieden zwischen einzelnen Instrumenten oder Geräten u.U. geringfügen.

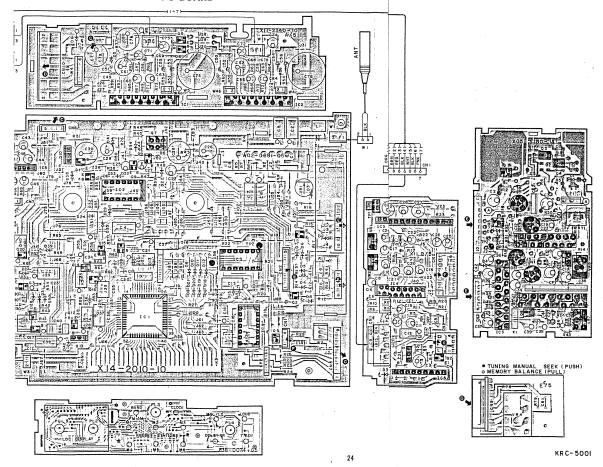
CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). A indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

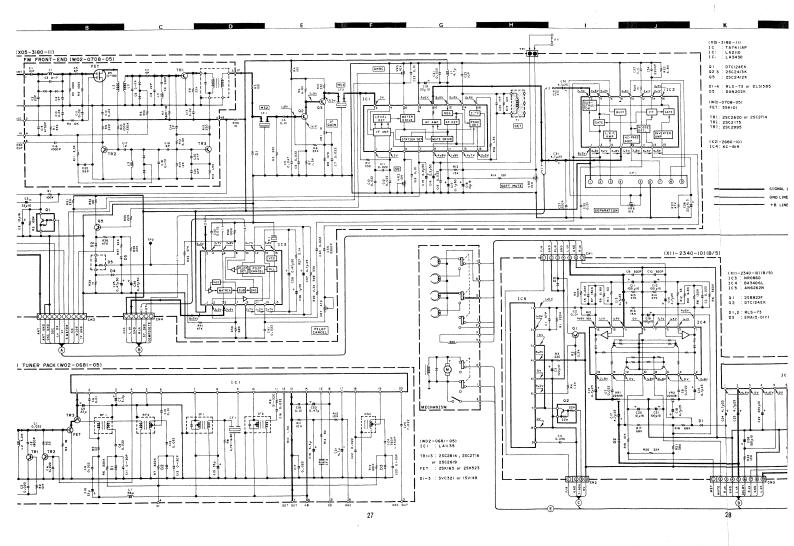


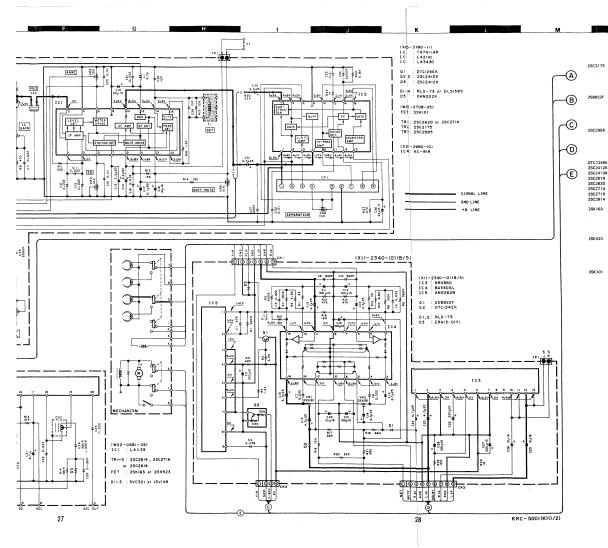




PC BOARD







DC voltages are as measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.

ANR262N

LA1135

LA2110 LA3430

BA3406L

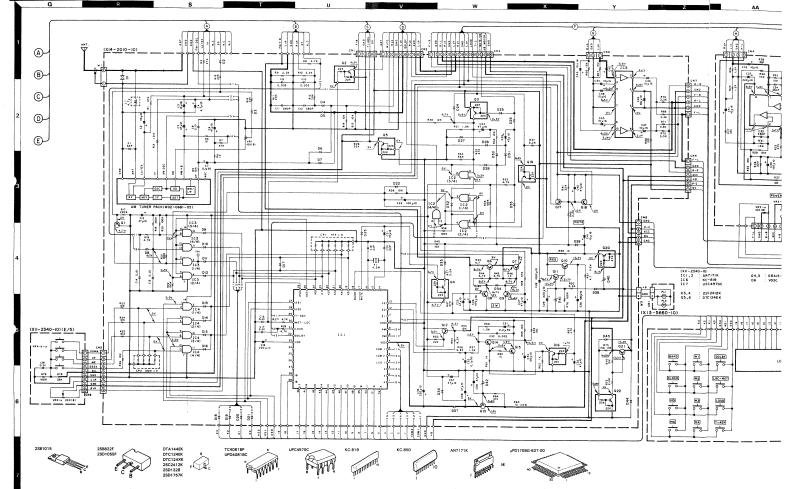
KC-819

Les tensions c.c. doivent être mesurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.

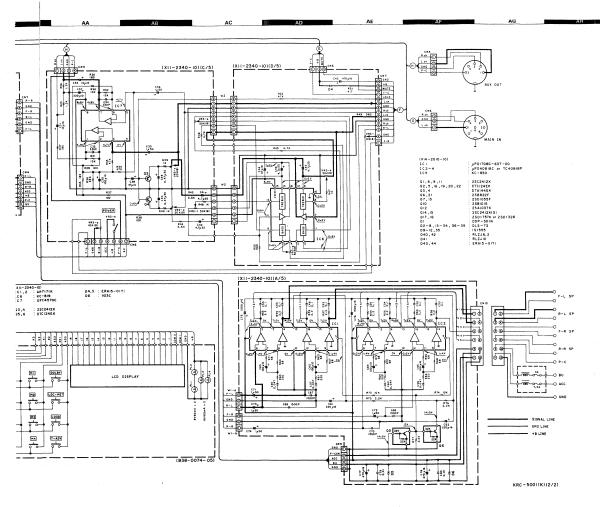
Die angegebenen Gleichspannungswerte wurden mit einem hochohmigen Spannungsmesser gemessen. Dabei schwanken die Meßwerte aufgrund von Unterschieden zwischen einzelnen Instrumenten oder Geräten u.U. geringfügig.

CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). A Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.





3)



DC voltages are as measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.

Les tensions c.c. doivent être mesurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes appareils et aux instruments de mesure individuels.

Die angegebenen Gleichspannungswerte wurden mit einem hochohmigen Spannungsmesser gemessen. Dabei schwanken die Meßwerte aufgrund von Unterschieden zwischen einzelnen Instrumenten oder Geräten U.U. geringfüglich.

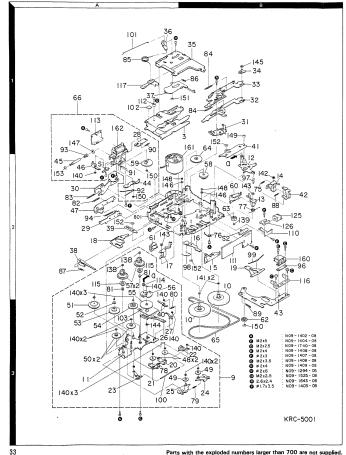
CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). A indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

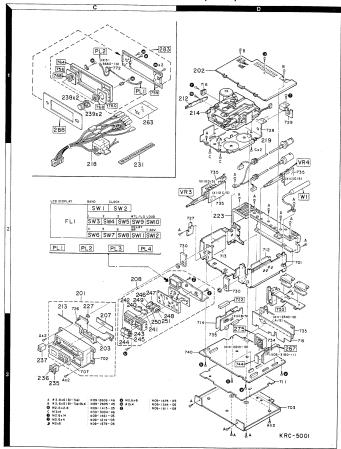


KRC-5001 KRC-5001

EXPLODED VIEW (MECHANISM)

EXPLODED VIEW (MAIN)





KRC-5001 KRC-5001

PARTS LIST

* New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Telle ohne Parts No. werden nicht geliefert.

Ref. No.	Address		Parts No.	Description	Desti- Re-
参照 番号	位置	Parts 新	部品番号	部品名/規格	nation mark 仕 向情考
			KRC	-5001	
201 202 203	2C 1D 3C	* *	A20-5094-02 A52-0100-12 A52-0102-03	PANEL ASSY TOP COVER FRONT BOARD	
207 208 - -	30 20	*	B11-0141-04 B38-0074-05 B46-0100-00 B46-0118-03 B50-6542-00	COLOR FILTER (CASSETTE LID) LIQUID CRYSTAL (LCD ASSY) WARRANTY CARD QUESTIONAIRE CARD INSTRUCTION MANUAL	
- FL1 PL3 +4	20	*	B58-0814-04 B58-0834-04 B38-0084-08 B39-1119-05	CAUTION CARD CAUTION CARD LIQUID CRYSTAL LAMP (LCD ASSY)	
212 213 214	1D 2C 1D	*	D10-1318-04 D21-0512-04 D40-0391-05	LEVER (EJECT) SHAFT CASSETTE MECHANISM ASSY	
218 219	1C 1D	*	E30-1526-05 E30-1527-05	DC CORD CORD WITH DIN CONNECTOR	
223 F1 F2	2D	*	F01-1135-05 F06-3026-05 F05-7521-05	HEAT SINK (REAR) FUSE (3A) DC CORD ASSY FUSE (7.5A) DC CORD ASSY	
227	20		G01-1958-04	TORSION COIL SPRING	
-		* *	H01-7371-04 H03-0861-04 H10-1862-13 H10-1895-03 H25-0029-04	ITEM CARTUN CASE BUTER CARTUN CASE POLYSTYRENE FOAMED FIXTURE POLYSTYRENE FOAMED FIXTURE PROTECTION BAG (60X110)	
-			H25-0103-04 H25-0117-04 H25-0226-04 H25-0234-04 H25-0268-04	PROTECTION BAG (125X250X0.07) PROTECTION BAG (80X250X0.07) PROTECTION BAG (180X300X0.05) PROTECTION BAG PROTECTION BAG	
231	20		J54-0059-04	STAY	
235 236 237 238 239	30 30 30 10	*	K27-1579-14 K27-1580-14 K27-1581-14 K29-1888-04 K29-1889-04	KN8B (BUTTON) FF KN8B (BUTTON) REW KN8B (BUTTON) EJECT KN8B (BAL,FADER) KN8B (TUNING,VOLUME)	
240 241 242 243 244	20 30 20 30 30	****	K27-1766-08 K27-1767-08 K27-1768-08 K27-1769-08 K27-1770-08	KNOB (BUTTON) BAND KNOB (BUTTON) CLOCK KNOB (BUTTON) MTL/LO KNOB (BUTTON) LOUD KNOB (BUTTON) LOUD	
245 246 247 248 249	30 20 20 20 30 20	* * * *	K27-1771-08 K27-1772-08 K27-1773-08 K27-1774-08 K27-1775-08	KNOB (BUTTON) T.ADV KNOB (BUTTON) 1 KNOB (BUTTON) 2 KNOB (BUTTON) 3 KNOB (BUTTON) 4	
250 251	30 30	*	K27-1776-08 K27-1781-08	KNOB (BUTTON) 5 KNOB (BUTTON) 6	

E: Scandinavia & Europe K: USA P: Canada W:Europe U: PX(Far East, Hawaii) T: England M: Other Areas

UE : AAFES(Europe) X: Australia

♠ indicates safety critical components.

PARTS LIST

× New Parts Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No, ne sont pas fournis,

Telle ohne Parts No. werden nicht geliefent.

Ref.	No.	Address		Parts No.	Description	Desti-	Re-
参照	番号	位置	Parts 新	部品番号	部品名/規格	nation 仕 向	mark 備考
263 B. J T	X	1C 1D 1D 1D 3C		N99-0066-15 N09-1415-05 N09-1461-05 N09-1214-05 N09-1579-05	SCREW SET MACHINE SCREW (M2. 6X2. B)EJECT STEPPED SCREW (M2. 6X14) TAPTITE SCREW (M2. 6X4) TAPTITE SCREW (M2. 8X8)		
V Z		3D 2D		N09-1672-05 N09-1811-05	TAPTITE SCREW (M2.6X6) SCREW (LCD ASSY)		
				TUNER UNIT	(X05-3180-11)	***************************************	
C2 C3 C4 C5 C6	.7		*	C90-0478-05 C90-0831-05 CK73FB1H103K CK73EB1H473K CK73FB1H223K	ELECTR® 10UF 16WV ELECTR® 33UF 10WV CHIP C 0.010UF K CHIP C 0.047UF K CHIP C 0.022UF K		
09 010 - 013 014 , 016			*	CE04DW1A101M CK73FB1H223K CK73FB1H103K C90~0508~05 CK73FB1H223K	ELECTRN 100UF 10WV CHIP C 0.022UF K CHIP C 0.010UF K ELECTRN 2.2UF 50WV CHIP C 0.022UF K		
C17 C18 C19 C21 C22			*	C90-0484-05 C90-0478-05 C90-0831-05 CK73FB1H223K CC73FRH1H100D	ELECTR® 0.47UF 50WV ELECTR® 10UF 16WV ELECTR® 33UF 10WV CHIP C 0.022UF K CHIP C 10PF D		
C23 C24 C25 C27 C30			*	CK73FB1H103K CC73FCH1H150J C90-0484-05 CC73FSL1H221J C90-0482-05	CHIP C 0.010UF K CHIP C 15PF J ELECTR8 0.47UF 50WV CHIP C 220PF J ELECTR8 4.7UF 25WV		
C32 , C34 C35 C36 C37	, 33			CK73FB1H103K CK73FB1H222K CK73FB1H332K CS15E1A220M C90-0482-05	CHIP C 0.010UF K CHIP C 2200FF K CHIP C 3300FF K TANTAL 22UF 10WV ELECTR® 4.7UF 25WV		
038 039 040 042 043	,41		*	CED4CW1H010M CE04CW1HR47M CK73FB1H223K CK73FB1E153K CC73FSL1H680J	ELECTRS 1.0UF 50WV ELECTRS 0.47UF 50WV CHIP C 0.022UF K CHIP C 0.015UF K CHIP C 68PF J	-	
CN1 CN2 W1			*	E40-3397-05 E40-3393-05 E31-3571-05	PIN ASSY PIN ASSY WIRING HARNESS		
CF1 : L1 T1 X1	,2		*	L72-0145-05 L40-4791-16 L30-0450-05 L78-0208-05	CERAMIC FILTER SMALL FIXED INDUCTOR(4.7UH,K) FM IFT RESSNATOR (18.950KHZ)		
	,2			R92-0670-05 R90-0282-05 RK73FB2A104J RK73FB2A101J RK73FB2A331J	CHIP R 0 8HM C8MP8SITE ELEMENTS CHIP R 100K J 1/10W CHIP R 100 J 1/10W CHIP R 330 J 1/10W		
R6 R7 R8 R9				RK73FB2A6B2J RK73FB2A152J RK73FB2A333J RK73FB2A471J	CHIP R 6.8K J 1/10W CHIP R 1.5K J 1/10W CHIP R 33K J 1/10W CHIP R 470 J 1/10W		-

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Ref. No.	Address	New	Parts No.	Description	Desti- Re- nation marks
參 瓶 香 号	位 置	9 5	部品番号	部 晶 名 / 規格	仕 向情考
R10 +11 R13 R14 R15 R16		*	RK73FB2A331J RK73FB2A273J RK73FB2A153J RK73FB2A123J RK73FB2A123J RK73FB2A132J	CHIP R 330 J 1/10W CHIP R 27K J 1/10W CHIP R 15K J 1/10W CHIP R 12K J 1/10W CHIP R 12K J 1/10W	
R20 R22 R23 R24 R27			RK73FB2A223J RK73FB2A6B2J RK73FB2A473J RK73FB2A102J RK73FB2A512J	CHIP R 22K J 1/10W CHIP R 6.8K J 1/10W CHIP R 47K J 1/10W CHIP R 1.0K J 1/10W CHIP R 5.1K J 1/10W	
R28 R29 R30 R31 R32		*	RK73F82A333J RK73F82A222J RK73F82A100J RK73F82A223J RK73F82A101J	CHIP R 33K J 1/10W CHIP R 2.2K J 1/10W CHIP R 10 J 1/10W CHIP R 22K J 1/10W CHIP R 100 J 1/10W	
R33 R34 R36 R37 +38 R39			RK73FB2A223J RK73FB2A102J RK73FB2A473J RK73FB2A223J RK73FB2A683J	CHIP R 22K J 1/10W CHIP R 1.0K J 1/10W CHIP R 47K J 1/10W CHIP R 22K J 1/10W CHIP R 68K J 1/10W	
R40 R41 +42 VR1 VR2 +3 VR5			RK73FB2A104J RK73FB2A473J R12-1054-05 R12-3071-05 R12-3100-05	CHIP R 100K J 1/10W CHIP R 47K J 1/10W TRIMMING P8T. (10K) SD. ANRC TRIMMING P8T. (10K) SD. ANRC TRIMMING P8T. (10K) SBFT MUTE	
VR6 VR7			R12-3071-05 R12-3103-05	TRIMMING PST. (10K)SEPARATISN TRIMMING PST. (47K)PILST CANCEL	
D1 -4 D1 -4 D5 IC1 IC2			DLS1585 RLS-73 DAN202K TA7411AP LA2110	DISDE DISDE DISDE ICCEM IF) ICCEM NOISE CANCELLER)	
103 01 92 +3 95			LA3430 DTC124EK 2SC2413K 2SC2412K	IC(FM MPX) DIGITAL TRANSISTØR TRANSISTØR TRANSISTØR	
267	3D	*	W02-0708-05	FM FRONT-END ASSY	
C1		_	CONTROL UNI	T (X11-2340-10)	
C2 C3 C4 C5 -8			CK73EB1H103K C90-1263-05 C092M1H563J C092M1H223J	CHIP C 0. GLOUF K ELECTRO LOGUF 16WV MYLAR 0. GS6UF J MYLAR 0. GS2UF J	
C9 ,10 C11 ,12 C13 -16 C17 ,18 C19			CK41DB1HB21K CE04DW1A101M C90-0482-05 CE04DW1E4R7M CE04DW1A101M	CYLND CHIP C 620PF K ELECTR® 100UF 10MV ELECTR® 4.7UF 25MV ELECTR® 4.7UF 25MV ELECTR® 100UF 10MV	
C20 C21 C22 C23 ,24 C25 ,26			CE04DW1C100M CE04DW1A101M CE04DW1A221M C90-0482-05 C90-0478-05	ELECTR8 10UF 16WV ELECTR8 100UF 10WV ELECTR8 220UF 10WV ELECTR9 4, 7UF 25WV ELECTR9 10UF 16WV	
				<u> </u>	<u> </u>

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参照看号	位置	Perts Fi	部品番号	部 品 名/規 格	住 向 備考
027 028 030 031 +32 -			CE04DW1A221M CE04DW1A103M CE04DW1A103M CE04DW1C100M CK41DB1H331K	ELECTRO 220UF 10WV ELECTRO 100UF 10WV ELECTRO 10UF 10WV ELECTRO 10UF 16WV CYLND CHIP C 330PF K	
C35 +36 C37 C38 C39 +40 C41 +42			CE04DW1HDR1M CE04DW1A221M CE04DW1A47DM CE04DW1HR47M CE04DW1E4R7M	ELECTRO	
043 044 045 ,46 049 -56 057 -60			CE04DW1C471M CE04DW1A470M C90-0482-05 CF92V1H104J CK73EB1H473K	ELECTRS 470UF 16WV ELECTRS 47UF 10WV ELECTRS 4.7UF 25WV MF 0.10UF J CHIP C 0.047UF K	
061 ,62 063 -66 067 -70 071 -74 075 ,76		*	CE04DW1C221M CE04DW1A101M C93-0002-05 CE04DW1H010M C90-1438-05	ELECTR® 220UF 16WV ELECTR® 100UF 10WV CHUP C 1500FF M ELECTR® 1.0UF 50WV ELECTR® 1500UF 16WV	
077 -78 079 -81			C90-1402-05 CK73EB1H103K	ELECTRO 470WV 16WV CHIP C B. DIOUF K	
CN1 CN2 CN3 CN4 CN5		* *	E40~3304~05 E40~3462~05 E40~3467~05 E40~3301~05 E40~3221~05	PIN ASSY PIN ASSY PIN ASSY PIN ASSY PIN ASSY	
CN6 CN7 CN8 CN9 CN10		*	E40-3300-05 E40-3227-05 E40-3223-05 E40-3463-05 E40-3719-05	PIN ASSY PIN ASSY PIN ASSY PIN ASSY PIN ASSY	
W1 W2 W3 W4 W5		***	E31-3922-05 E31-3767-05 E31-3768-05 E31-3921-05 E31-3575-05	WIRING HARNESS WIRING HARNESS WIRING HARNESS WIRING HARNESS WIRING HARNESS	
J1 -19 J28 J45 ,46 R1 R2		*	R92-0338-05 R92-0150-05 R92-0338-05 R0410B2B224J RD410B2B332J	CLYND CHIP R 0 8HM JUMPER REST 0 8HM CLYND CHIP R 0 8HM CYLND CHIP R 220K J 1/8W CYLND CHIP R 3.3K J 1/8W	
R3 R4 R5 +6 R7 +8 R9 +10			RD41DB2B221J RD41DB2B122J RD41DB2B564J RD41DB2B154J RD41DB2B682J	CYLND CHIP R 220 J 1/8W CYLND CHIP R 1.2K J 1/8W CYLND CHIP R 560K J 1/8W CYLND CHIP R 150K J 1/8W CYLND CHIP R 6.8K J 1/8W	
R11 ,12 R13 ,14 R15 ,16 R17 ,18 R19			RD41DB2B330J RD41DB2B473J RD41DB2B163J RD41DB2B683J RD41DB2B103J	CYLND CHIP R 33 J 1/8W CYLND CHIP R 47K J 1/8W CYLND CHIP R 16K J 1/8W CYLND CHIP R 68K J 1/8W CYLND CHIP R 10K J 1/8W	
R20 R21			RD41DB2B223J RD41DB2B391J	CYLND CHIP R 22K J 1/8W CYLND CHIP R 390 J 1/8W	

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多無無零	位 置	Perts	部品番号	部品名/規格	nation mar 仕 向 第
R22 R25 ,26 R27 ,28 R29 ,30 R31 ,32			RD41DB2B103J RD41DB2B122J RD41DB2B181J RD41DB2B103J RD41DB2B153J	CYLND CHIP R 10K	
R33 +34 R35 R36 R37 +38 R43 +44			RD41DB2B473J RD41DB2B183J RD41DB2B223J RD41DB2B101J RD41DB2B562J	CYLND CHIP R 47K	
R47 +48 R49 +50 R51 R52 R53 -60			RD41DB2B102J RD41DB2B561J RD14DB2H4R7J RD41DB2B473J RD41DB2B2R2J	CYLND CHIP R 1.0K J 1/8W CYLND CHIP R 560 J 1/8W SMALL-RD 4.7 J 1/2W CYLND CHIP R 47K J 1/8W CYLND CHIP R 2.2 J 1/8W	
R61 -64 R65 ,66 R67 -70 R71 ,72 R73 ,74			RD41DB28221J RD41DB2B680J RD41DB2B560J RK73FB2A680J RK73FB2A1D3J	CYLND CHIP R 220 J 1/8W CYLND CHIP R 68 J 1/8W CYLND CHIP R 56 J 1/8W CHIP R 68 J 1/10W CHIP R 10K J 1/10W	
R75 -77 R78 R79 VRI .2 VR3	3¢	:	RD41DB2B222J RD41DB2B333J RD41DB2B1O4J R12-3101-05 R24-9019-05	CYLND CHIP R 2.2K J 1/6W CYLND CHIP R 33K J 1/6W CYLND CHIP R 100K J 1/6W TRIMMING P0T. (22K)DSLBY LEVEL P0TENTISMETER(10KB)P0WER	
UR4	20		R29-3020-05	PRITENTINMETER (20KB) BALANCE	
D1 .2 D3 -5 D6 IC1 .2 IC3		*	RLS-73 ERA15-01Y1 V03C AN7171K NR0860	DISDE DISDE DISDE DISDE ICCAUDIS POWER AMP) ICCOSLBY)	
IC4 IC5 IC6 IC7 91			BA3406L AN6262N KC-819 UPC4570C 258822F	IC(PREAMP FOR TAPE EQ X2) IC(T.ADV) IC(TONE AMP X2) IC(OP AMP X2) TRANSISTOR	
02 03 ,4	-		DTC124EK 2SC2412K	DIGITAL TRANSISTOR TRANSISTOR	
95 -6		<u>.</u>	SYNTHESIZER U	DIGITAL TRANSISTOR FNIT (X14-2010-10)	
C1 · C2 C4 C5 ·6 C7			CC41DSL1H330J CE04DW1A330M CE04DW1E4R7M CE04DW1A470M CE04DW1H0R1M	CYLND CHIP C 33PF J ELECTR9 33UF 10MV ELECTR8 4.7UF 25MV ELECTR8 47UF 10MV ELECTR9 0.1UF 50MV	
C8 09 ,10 C11 ,12 C13 C14 -17			C93-0012-05 CK73EB1H333K CK41DB1H661K CE04DW1A470M C93-0012-05	CYLND CHIP C 0.01UF M CHIP C 0.033UF K CYLND CHIP C 680PF K ELECTRO 47UF 10WU CYLND CHIP C 0.01UF M	
C18 C19 ,20 C21 C22	-		CE04DW1A221M CC73FCH1H220J CE04DW1C100M CE04DW1A101M	ELECTR® 220UF 10WU CHIP C 22PF J ELECTR® 10UF 16WU ELECTR® 100UF 10WU	

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参照番号	位置	新	部品番号	部 品 名/規 格	仕 向衛
C23 -25 C26 C27 C28 C29			CEO4DW1A470M CEO4DW1A471M CEO4DW1A470M CEO4DW1A102M CEO4DW1A221M	ELECTR8 47UF 10WV ELECTR8 470UF 10WV ELECTR8 47UF 10WV ELECTR8 1000UF 10WV ELECTR8 220UF 10WV	
630 631 632 633 634			CE04DW10102M CE04DW1H2R2M CK73EB1H223K CE04DW1H0R1M CE04DW1A101M	ELECTRS 1000UF 16WV ELECTRS 2.2UF 50WV CHIP C 0.022UF K ELECTRS 0.1UF 50WV ELECTRS 100UF 10WV	
035 036 037 ~40 041 042 ~48			C90-0822-05 CE04DW1A221M CE04DW1A470M CE04DW1C100M C90-0478-05	ELECTR9 470F 1640V ELECTR9 220UF 1040V ELECTR9 470F 1040V ELECTR9 10UF 1640V ELECTR9 10UF 1640V	
C49			CEO4DW1A101M	ELECTRO 1000F 10WV	
CN1 CN2 CN3 CN4 CN5		* *	E40-3483-05 E40-3488-05 E40-3235-05 E40-3230-05 E40-3231-05	PIN ASSY PIN ASSY SBCKET FBR PIN ASSY SBCKET FBR PIN ASSY SBCKET FBR PIN ASSY	
ENG ENR ENB W1 W2	20	*	E40-3238-05 E40-3241-05 E40-3484-05 E30-1529-05 E31-3923-05	PIN ASSY PIN ASSY PIN ASSY CRRD WITH PLUG WIRING HARNESS	
W3		*	E31~3795-05	WIRING HARNESS	
L1 X1			L39-0129-05 L77-0585-05	TRAP CBIL CRYSTAL RESONATOR(4, SMHZ)	
CP1 CP2 J1 -21 J23 -44 J67			R90-0450-05 R90-0254-05 R92-0338-05 R92-0338-05 R92-0150-05	MULTIPLE RESISTOR COMPOSITE ELEMENTS CLYND CHIP R O 8HM CLYND CHIP R O 8HM JUMPER REST O 8HM	
J72 J83 R2 R3 R4			R92-0150-05 R92-0150-05 R0410828103J RD410828273J RD410828100J	JUMPER REST 0 9HM JUMPER REST 0 8HM CYLND CHIP R 10K J 1/8W CYLND CHIP R 27K J 1/8W CYLND CHIP R 10 J 1/8W	
R5 R6 R7 R8 R9 .1B			RD41DB2B103J RD41DB2B102J RD41DB2B473J RD41DB2B472J RD41DB2B222J	CYLND CHIP R 10K J 1/8W CYLND CHIP R 1.0K J 1/8W CYLND CHIP R 47K J 1/8W CYLND CHIP R 4.7K J 1/8W CYLND CHIP R 2.2K J 1/8W	
R11 +12 R13 +14 R15 +16 R17 R18			RD41DB2B2O3J RD41DB2B1Q3J RD41DB2B223J RD41DB2B224J RD41DB2B472J	CYLND CHIP R 20K	
R19 ~22 R23 ~26 R28 R29			RD41DB2B104J RD41DB2B103J RD41DB2B472J RD41DB2B23J	CYLND CHIP R 100K J 1/8W CYLND CHIP R 10K J 1/8W CYLND CHIP R 4.7K J 1/8W CYLND CHIP R 22K J 1/8W	

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参照番号	位置	##rts	部品番号	部品名/規格	nation 仕 向	mar l 備相
R31 R32 R33 R34 R35			RD410828152J RD410828473J RD410828563J RD410828103J RD410828472J	CYLND CHIP R 1.5K J 1/8W CYLND CHIP R 47K J 1/8W CYLND CHIP R 56K J 1/8W CYLND CHIP R 10K J 1/8W CYLND CHIP R 4.7K J 1/8W		
R36 R37 R38 R39 ,40 R41			RD410828104J RD410828222J RD410828104J RD410828103J RD410828223J	CYLND CHIP R 100K		
R42 R43 R44 ,45 R46 R47 ,48			RD41DB2B222J RD41DB2B4R7J RD41DB2B472J RD41DB2B1G2J RD41DB2B473J	CYLND CHIP R 2.2K J 1/8W CYLND CHIP R 4.7 J 1/8W CYLND CHIP R 4.7K J 1/8W CYLND CHIP R 1.0K J 1/8W CYLND CHIP R 47K J 1/8W		
R50 R51 R52 R53 R54			RD41DB2B562J RD14DB2H4R7J RD41DB2B102J RD41DB2B223J RD41DB2B822J	CYLND CHIP R 5.6K		
R55 R56 R57 ,58 R59 R60 ~62			RD41DB2B103J RD41DB2B682J RD41DB2B152J RD41DB2B103J RD41DB2B102J	CYLND CHIP R IOK J 1/8W CYLND CHIP R 6.8K J 1/8W CYLND CHIP R 1.5K J 1/8W CYLND CHIP R IOK J 1/8W CYLND CHIP R I.OK J 1/8W		
R63 R64 R66 R67 ,68 R67 -72			RD41DB2B473J RD41DB2B103J RD41DB2B101J RD41DB2B153J RD41DB2B332J	CYLND CHIP R 47K		
R73 R74 R75 R76 R77			RD41DB2B222J RD41DB2B103J RS14DB3D151J RD41DB2B104J RD41DB2B222J	CYLND CHIP R 2.2K		
VR1			R12-3096-05	TRIMMING POT. (10K)STOP LEVEL		
D1 D2 -8 D9 -12 D13 -34 D35			DSP-301N RLS-73 191555 RLS-73 191555	SURGE ABSORBER DISDE DISDE DISDE DISDE		
036 -39 040 041 042 043 ,44			RLS-73 RLZJ6, 2 RLZJ10 RLZJ6, 2 ERA15-01Y1	DIODE ZENER DIODE ZENER DIODE ZENER DIODE DIODE DIODE		
IC1 IC2 -4 IC2 -4 IC5 IC5			UP01708G-437-00 TC40818P UP04081BC KC-850 2SC2412K	IC(DIGITAL TUNING SYSTEM) NNT) IC(AND X4) IC(AND X4) IC(ISQLATION AMP) TRANSISTOR		
92 03 ,4 95			DTC124EK DTA144EK DTC124EK	DIGITAL TRANSISTOR DIGITAL TRANSISTOR DIGITAL TRANSISTOR		

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参照番号	位置	5	部品番号	部 品 名 / 規 格	任 向	強考
06 97 08 ,7 010 011			2SB822F 2SD1055F 2SC2412K 2SB1015 2SC2412K	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR		
012 013 014 ,15 016 017 ,18	Andread Andreas and Andreas An		2SA1037K 2SD1055F 2SC2412K(S) DTC124EK 2SD1328	TRANSISTØR TRANSISTØR TRANSISTØR DIGITAL TRANSISTØR TRANSISTØR		
017 ,18 019 920 921 922		*	2SD1757K DTC124EK DTC124XK 2SB822F DTC124EK	TRANSISTØR DIGITAL TRANSISTØR DIGITAL TRANSISTØR TRANSISTØR DIGITAL TRANSISTØR		
275	3D		W02-0681-05	TUNER ASSY		
				Y UNIT (X90-2440-10)		
283	10	*	807-1732-03	ESCUTCHENN ASSY		
288	10		F39~0021-03	REINFORCING PLATE		
-			H25-0117-04	PRETECTION BAG (80X250X0.07)		
W PL1 PL2	10 10 10	*	N09-1344-05 B30-1135-15 B30-1125-15	TAPTITE SCREW (02X4) LAMP (R) LAMP (L) /		
CN1		*	E40-0287-05	PIN ASSY		<u> </u>
			TUNER ASS'Y (W02-0681-05)		
D1 -3 D1 -3 FET1 FET1 IC1			SVC321 1SV149 2SK163 2SK523 LA1135	DIODE DIODE FET FET IC(AM)		-
TR1 -3 TR1 -3 TR1 -3			25C2619 25C2716 25C2814	TRANSISTOR TRANSISTOR TRANSISTOR		
FM FRONT-END ASS'Y (W02-0708-05)						
FET1 TR1 TR2 TR2 TR3		*	35K101 25C2620 25C2175 25C2714 25C2995	FET TRANSISTØR TRANSISTØR TRANSISTØR TRANSISTØR		
SCREW SET (N99-0066-15)						
-			N09-0334-05 N09-0335-05 N09-0366-05 N10-1050-46 N14-0131-05	SCREW (M5X8) SCREW (M5X16) SCREW (M5X20) HEXAGON NUT NUT		
			N19-0337-05	FLAT WASHER		
CASSETTE MECHANISM ASS'Y (D40-0391-05)						
3	18		A53-0674-08	CASSETTE HOLDER		-

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参照著号	位置	rarts \$∏	部品署号	部品名/規格	nation man 任 向 儒
9 10 11 12 13	38 28,38 3A 28 28		003-0241-08 001-0073-08 003-0229-08 010-1319-08 010-1321-08	REEL DISK ASSY FLYWHEEL ASSY SLIDER ASSY (MAIN GEAR) SLIDER ASSY (SWITCHING) LEVER (TRIGGER STOP)	
14 15 16 17 18	2B 2B 2B 2A 2A		D10-1322-08 D10-1323-08 D10-1324-08 D10-1651-08 D10-1326-08	ARM (FF REW LBCK) SLIDER (FF REW) LEVER (FF REW BP) ARM SLIDER (TAKE UP GEAR PUSH	
19 20 21 22 23	28 3A 3A 3B 3B		D10-1328-08 D10-1329-08 D10-1330-08 D10-1331-08 D10-1332-08	ARM (PINCH RÖLLER ÖP) SLIDER ASSY (FF GEAR) SLIDER ASSY (REW GEAR) ARM (END DETECT.F) ARM (END DETECT.R)	
24 25 26 27 28	38 3A 3A 3A 1A		D10-1333-08 D10-1334-08 D10-1335-18 D10-1336-08 D10-1337-09	SLIDER ASSY (TAKEUP GEAR,F) SLIDER ASSY (TAKEUP GEAT,R) SLIDER (END SENSOR) ARM (TRIGGER) LEVER (SW 8P)	
29 30 31 32 33	2A 2A 1B 1B 1B		D10-1338-08 D10-1340-08 D10-1652-08 D10-1654-08 D10-1653-08	SLIDER ASSY (PUSH) LEVER (LIFT UP) BRACKET ASSY (FF/REW) LEVER (REW) LEVER (FF)	
34 35 36 37 38	18 18 1A 1A 2A		D10-1344-08 D10-1345-08 D10-1346-08 D10-1347-08 D10-1348-08	SLIDER (PR®G CHANGE) CASE LIFTER SLIDER ASSY (PACK EJECT) SLIDER (CASET DETECT) LEVER (TIMING)	
37 40 41 42 43	28 38 18 28 38		D10-1349-08 D10-1350-08 D10-1530-08 D10-1531-08 D10-1532-08	ARM (TAKEUP GEAR 8P ARM (ST8P) SLIDER (MAIN) ARM (FF/REW RELEASE HEAD PANEL ASSY	
44 45 46 47 48	2A 2A 2A 2A 2B		010-1533-08 010-1534-08 010-1535-18 010-1536-18 013-0185-08	SLIDER ASSY (KEY 0FF) PLUNGER (KEY 0FF) SLIDER (TRIGGER ARM) SLIDER ASSY (HALF/HEAD PUSH GEAR ASSY (FF)	
49 50 51 52 53	3A 3B 3A 3A 3A 3A 3A		D13-0186-08 D13-0187-18 D13-0188-08 D13-0189-18 D13-0190-18	GEAR	
54 55 56 57 58	3A 2A 2A 2A 2A 2B		D13-0191-08 D13-0192-08 D13-0193-08 D13-0194-08 D13-0331-18	GEAR (DEVICE TRIGGER/ST&P GEAR (TRIGGER/ST&P &P) GEAR (INVERTER) REEL DISK ASSY (TAKEUP) GEAR (MAIN)	
59 60 61 62 63	2A 2B 2A 3B 2B	*	D13-0309-08 D14-0114-08 D14-0115-08 D14-0131-08 D15-0228-18	GEAR (KEY 8FF,CAM) PINCH ROLLER ASSY(F) PINCH ROLLER ASSY(F) IDLER (HEAD PANEL) PULLEY (INTER MEDIATE)	

E: Scandinavia & Europe K: USA

P: Canada W:Europe

⚠ indicates safety critical components.

U: PX(Far East, Haweii) T: England M: Other Areas

UE: AAFES(Europe)

X: Australia



* New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Ref. No.	Address		Parts No.	Description	Desti-	Re-
参照番号	位置	Parts ∰	部品番号	部品名/規格	nation 仕 向	mark 備考
64 65 66	1B 3B 1A		D13-0332-08 D16-0109-18 D40-0349-08	GEAR BELT (MAIN) MECHANISM ASSY (KEY 8FF)		
75 76 77 78 79	28 28 28 3A 38		G01-1560-08 G01-1561-08 G01-1562-08 G01-1564-18 G01-1565-08	TENSION SPRING (FF/REW LOCK) TORSION COIL SPRING(CONTROL) TORSION COIL SPRING(TREF/STOP) TENSION SPRING (FF/REW GEAR) TENSION SPRING (TAKEUP GEAR)		
90 91 82 83 84	2A 2A 2A 2A 1B		G01-1566-08 G01-1567-08 G01-1740-08 G01-1571-08 G01-1572-08	TENSION SPRING (TRIGGER STOP) COMPRESSION SPRING(END DETECT) TENSION SPRING (HALF/HEAD PML) TENSION SPRING (LIFT UP LEVER) TENSION SPRING (FF/REW LEVER)		
85 86 87 88 89	1A 1B 2A 2B 3B		G01-1573-08 G01-1574-08 G01-1575-08 G01-1734-08 G01-1735-08	TBRSION COIL SPRING(INVERTER) TENSION SPRING (CASET DETECT) TENSION SPRING (TIMING LEVER) TENSION SPRING (FF RELEASE ARM TENSION SPRING (HEAD PANEL)		
90 91 92 93 94	1A 2A 2A 1A 29		601-1736-08 601-1737-08 601-1738-08 601-1739-08 601-1569-08	TENSIBN SPRING (PØWER SWITCH) TBRSIBN CBIL SP(KEY BFF GEAR) TBRSIBN CBIL SPRING(KEY BFF) CBMPRESSIBN SPRING(PLUNGER) TENSIBN SPRING (PUSH LEVER)		
96 97 98 99 100	28 28 28 28 28 3A		G02-0174-08 G09-0047-08 G09-0048-09 G09-0049-08 G09-0050-08	FLAT SPRING (PB HEAD) FORMED WIRE (HEAD SW) FORMED WIRE (FF/REW DP) FORMED WIRE (FINCH ROLLER) ROD (END SENSOR PUSH)		
101 102 103	1A 1A 3A		609-0051-08 613-0167-08 616-0112-08	FORMED WIRE (PACK EJECT) CUSHION SHEET (SLIP)		
110 111 112 113 114	2B 2B 1A 1A 2A	*	J19-2560-08 J25-5588-08 J32-0306-08 J25-4671-08 J31-0242-08	BRACKET (PLUNGER) PRINTED WIRING BOARD (HEAD) BOSS PRINTED WIRING BOARD (BASE) COLLAR (INVERTER GEAR)		
115 116 117	2A 2B 1A		J31-0243-08 J90-0149-08 J90-0150-18	COLLAR (END DETECT) GUIDE (TAPE) SLIDER (PACK)		
125 126	2B 2B		L900001-08 L92-0015-08	COIL ASSY (T)		
138 139 140 141 142	2A 2B 3A,2B 2B 2B		N19-1020-08 N19-1015-08 N19-0894-08 N19-0895-08 N19-0896-08	FLAT WASHER FLAT WASHER FLAT WASHER FLAT WASHER (FLYWHEEL) FLAT WASHER(REEL ASY, LOCK PLT)		
143 144 145 146 147	2A,28 3A 18 2A,28 1A		N19-0897-08 N19-0898-08 N19-0899-08 N19-0901-08 N19-0941-08	FLAT WASHER (PINCH ROLLER ASSY) FLAT WASHER (GEAR 59) FLAT WASHER (PC PLATE 91) FLAT WASHER WASHER (Ø3.6X8X0.2)		
148 149	2A,2B		N19-0942-08 N29-0082-08	FLAT WASHER (Ø1,55XØ3,5X0,5 E TUPE RETAINING RING(Ø1,5)		

E: Scandinavia & Europe K: USA U: PX(Far East, Hawaii) T: England M: Other Areas

P: Canada W:Europe

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UE: AAFES(Europe) X: Australia



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Les articles non mentionnes dans le Parts No. ne sont pes fournis.

Telle ohne Parts No. werden nicht gellefert.

Ref. No.	Address		Parts No.	Description	Desti-	Re-
参照番号	位置	新	部品番号	部品名/規格		mark: 備考
150 151 152 153 A	2A,3B 1A 2A,1B 2A 2B		N24-3012-46 N24-3015-46 N24-3020-46 N24-3025-45 N09-1402-08	E TYPE RETAINING RING(01, 2) E TYPE RETAINING RING(01, 5) E TYPE RETAINING RING(02) E TYPE RETAINING RING(02) SCREW (COLLER)		
C D E F G	28 2A,28 28 2A,28 3A		N09-1404-08 N09-1740-08 N09-1406-08 N09-1407-08 N09-1408-08	SCREW (M2X5) TAPE GUIDE 31 SCREW (M2X2, S)MOTOR TIMING LVR SCREW (M2X4) PB HEAD 33 SCREW (G2X3)PM BRCKT 70,PCB 20 SCREW (M2X3,S) MG PLT ASY 40		
H P Q R Y	1A-1B 2B 1A 1B 1A	*	N09-1409-08 N09-1294-05 N09-1525-08 N09-1643-08 N09-1403-08	SCREW (02X4)LIFTER 93,BRCKT 88 SCREW (02X6) SCREW (02X2,5) SCREW (M2,6X4,5) SCREW (M1,7X3,5)		
S1 S20	2A 2B	*	\$46-1081-05 \$31-3006-08	LEAF SWITCH (MUTING) SLIDE SWITCH		
160 161 162	2B 1B 1A	*	T31-0040-08 T42-0090-18 T94-0089-08	PLAYBACK HEAD DC MOTOR ASSY SOLENGID		
				·		
-						
·						
	参照 養号 150 151 152 153 A C D E F G H P P 9 R Y Y S1 S20 160 160	# # # # # # # # # # # # # # # # # # #	### ## ### ### #### ##################	The state of the	150	150

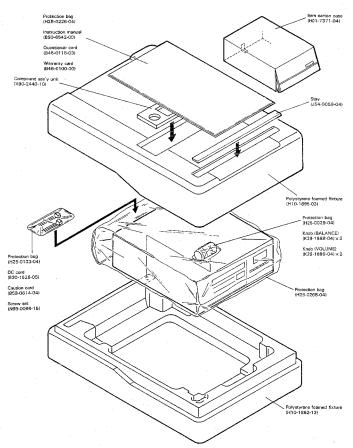
E: Scandinavia & Europe K: USA
U: PX(Far East, Hawaii) T: England
UE: AAFES(Europe) X: Australia

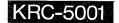
P: Canada W:Euro

★ indicates safety critical components.



PACKING





SPECIFICATIONS

FM Tuner Section
Frequency Range (200 kHz space)87.9 MHz ~ 107.9 MHz
(50 kHz space)87.5 MHz ~ 108.0 MHz
Channel Space
Usable Sensitivity
50 dB Quieting Sensitivity19.0 dBf (2.4 μV/75 ohms
Frequency Response ('±3 dB)30 Hz ~ 15 kHz
Signal to Noise Ratio70 dE
Alternate Channel Selectivity
Capture Ratio
Image Response Ratio65 d8
IF Response Ratio70 dE
Stereo Separation (1 kHz)40 dE
AM Tuner Section
Frequency Range (10 kHz space)530 ~ 1,620 kHz
(9 kHz space)
Channel Space
Usable Sensitivity (30 µV)30 dE
Cassette Deck Section
Tape Speed4.76 cm/s
Wow and Flutter0.12% iWRMS
Fast Winding Time (C-60)
Frequency Response (120 μs)40 Hz ~ 14 kHz (±3 dB)
(70 ss)

Stereo Separation (1 kHz)	
Audio Section	
Maximum Power Output	
(1 kHz, 4 ohrns)	20 W + 20 W
Rated Output Power	
(10%THD, 1 kHz, 4 ohms)	15 W + 15 W
(1 %THD, 30 Hz ~ 20 kHz, 4 ohm	is)10 W + 10 W
Tone Action	Treble: 10 kHz ±10 dB
General	
Operating Voltage (GND)	14,4 V (11 ~ 16 V)
Current Consumption	
Body Size (W × H × D)	180 × 50 × 140 mm
	(7-1/16×2-15/16×5-1/2 in.)
Weight	1.8 kg (4.0 lb)

Kenwood follows a policy of continuous advancements in development. For this reason specifications may be changed without notice.

Kenwood poursuit une politique de progrés constants en ce qui doncerne le développement. Pour cette raison, les spécifications pont ayietres à modifications assa prévis; Kenwood strebt ständige, Verbesserungen in der Entwicklung an Daher bielben Anderungen der technischen Daten jaderzeit vorgehalten.

KENWOOD CORPORATION Shionogi Shibuya Bultding, 17-5, 2-chome Shibuya, Shibuyaku, Tokyo 150, Japan

KENWOOD ELECTRONICS

1315 E. Wissoncherler BL Careary. Celifornia 90745;
75 Saralwa Drive, Secaucus, New Jarsey 07094, U.S.A.
KENWOOD ELECTRONICS CANADA INC.
1070 Jayson Court, Mississauga, Criario, Canada Lew 2V5
KENWOOD ELECTRONICS SENELLUX N.V.
Monchelsessenway 818 9-1903 Zawentern, Belgigan
KENWOOD ELECTRONICS DEUTSCHLAND GWBH
KENWOOD ELECTRONICS DEUTSCHLAND GWBH
TRICH-CHAND GWBH THE CONTROL OF THE CONTR